

*System And Method For Real Time Monitoring And  
Control Of Networked Computers*

Inventor: Robert F. Terry

Serial No.: 09/827,891

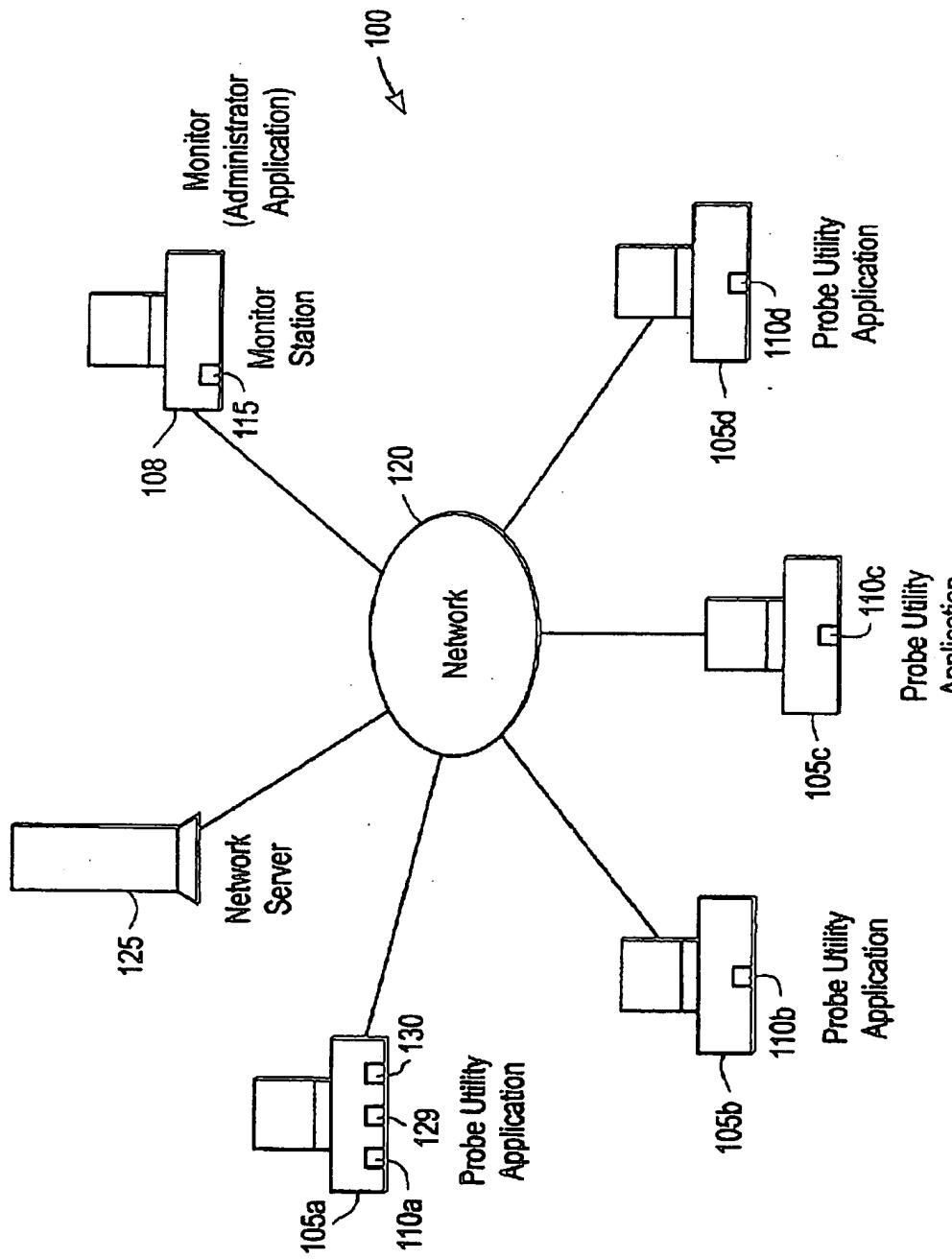


FIG. 1

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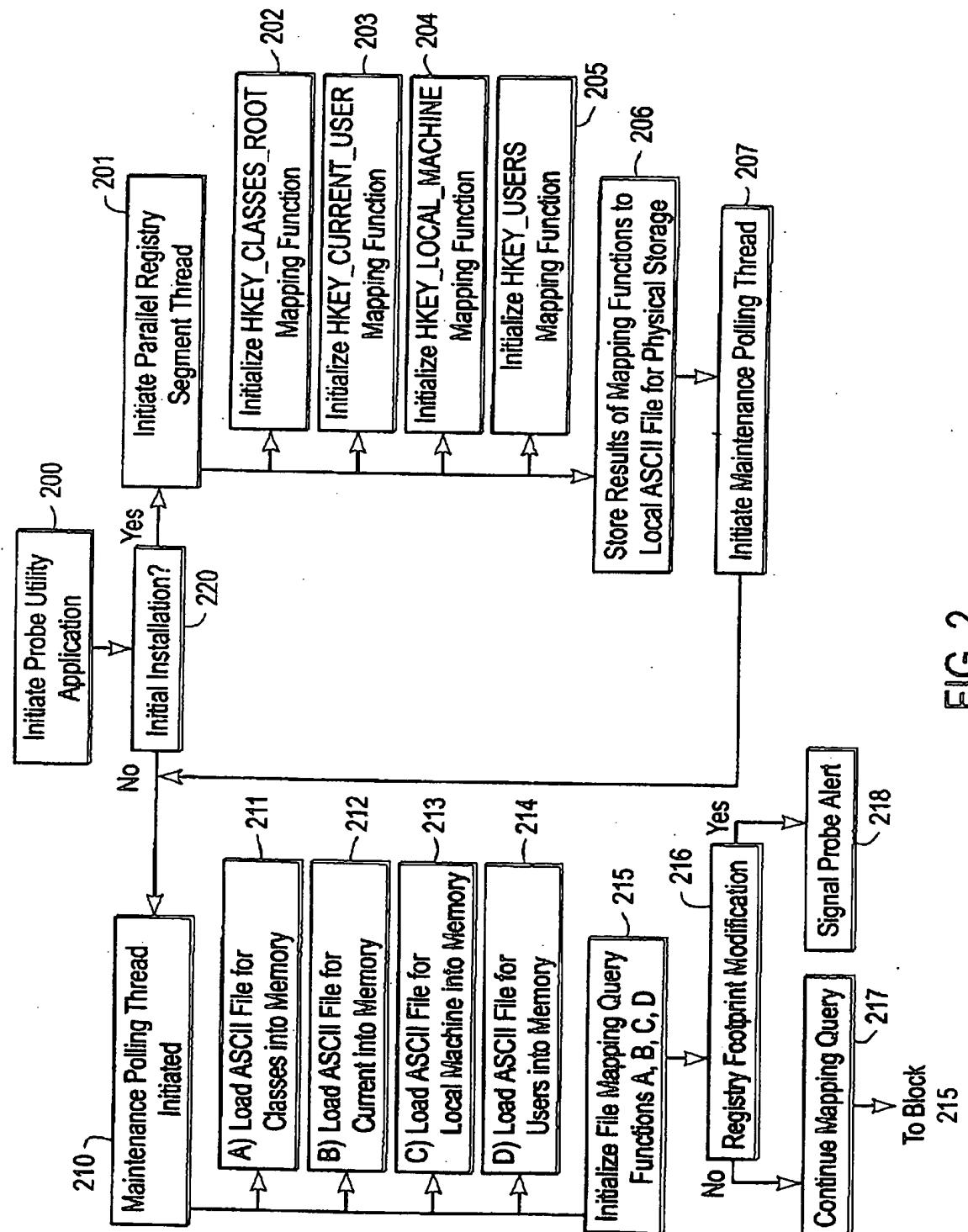


FIG. 2

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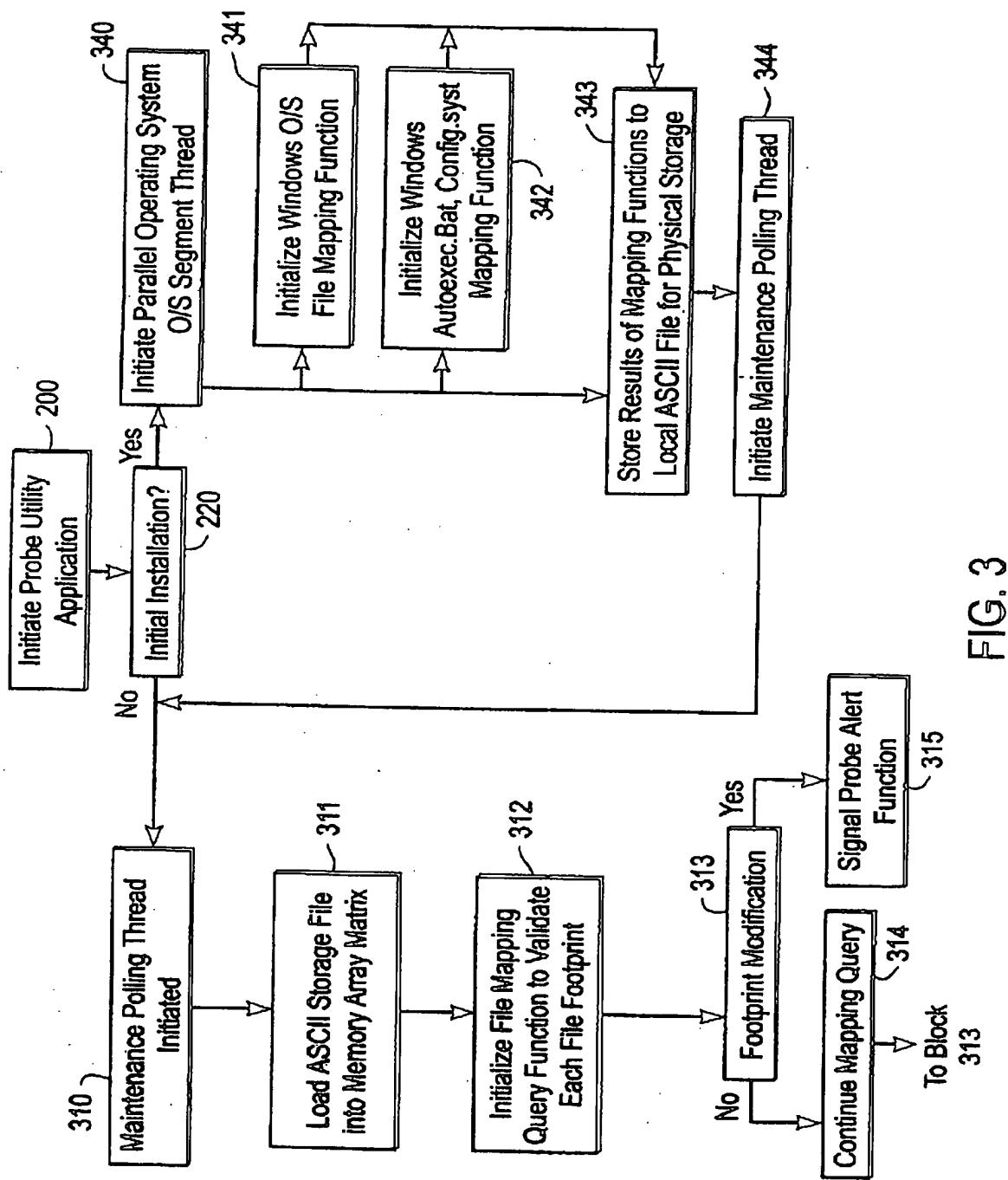


FIG. 3

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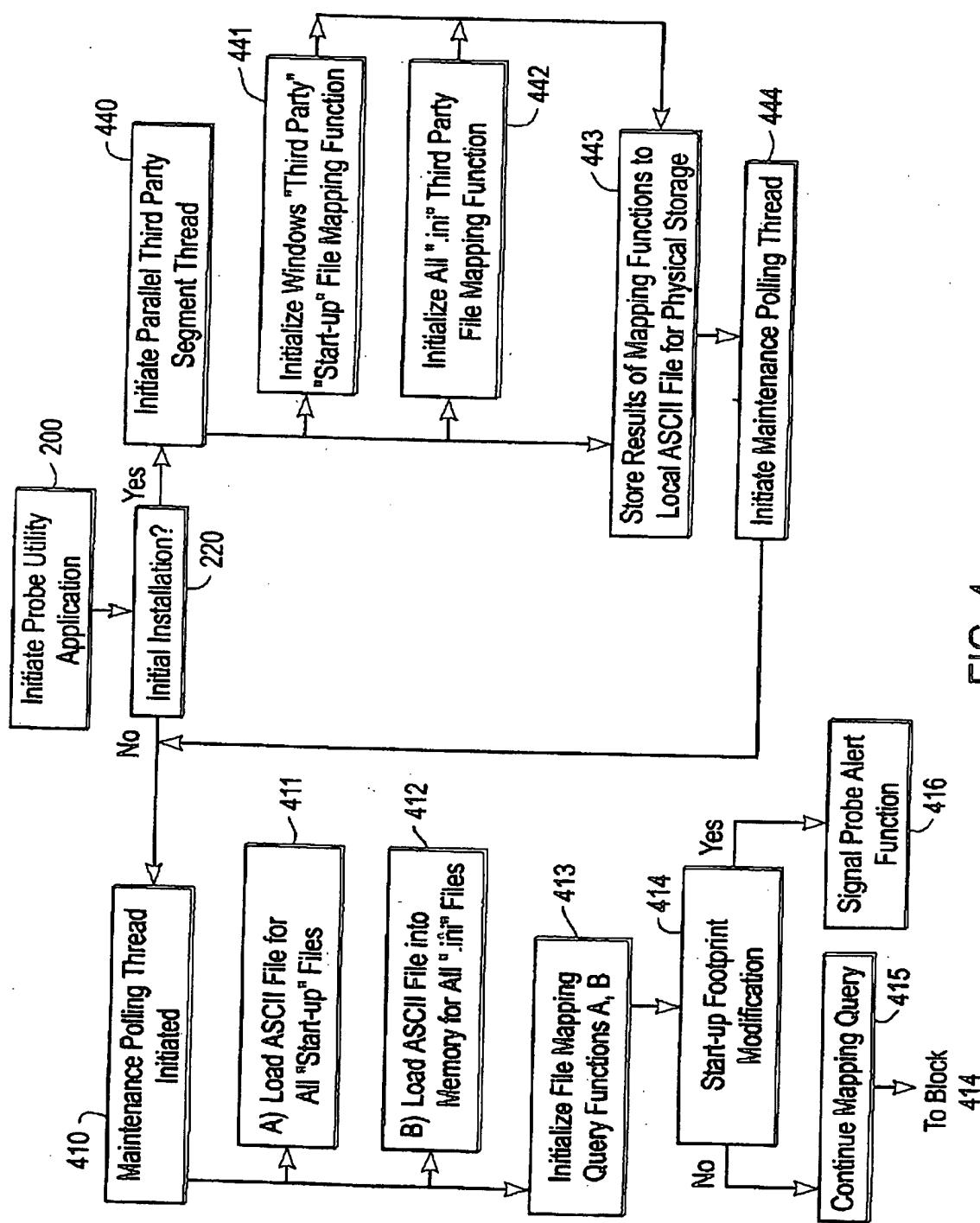


FIG. 4

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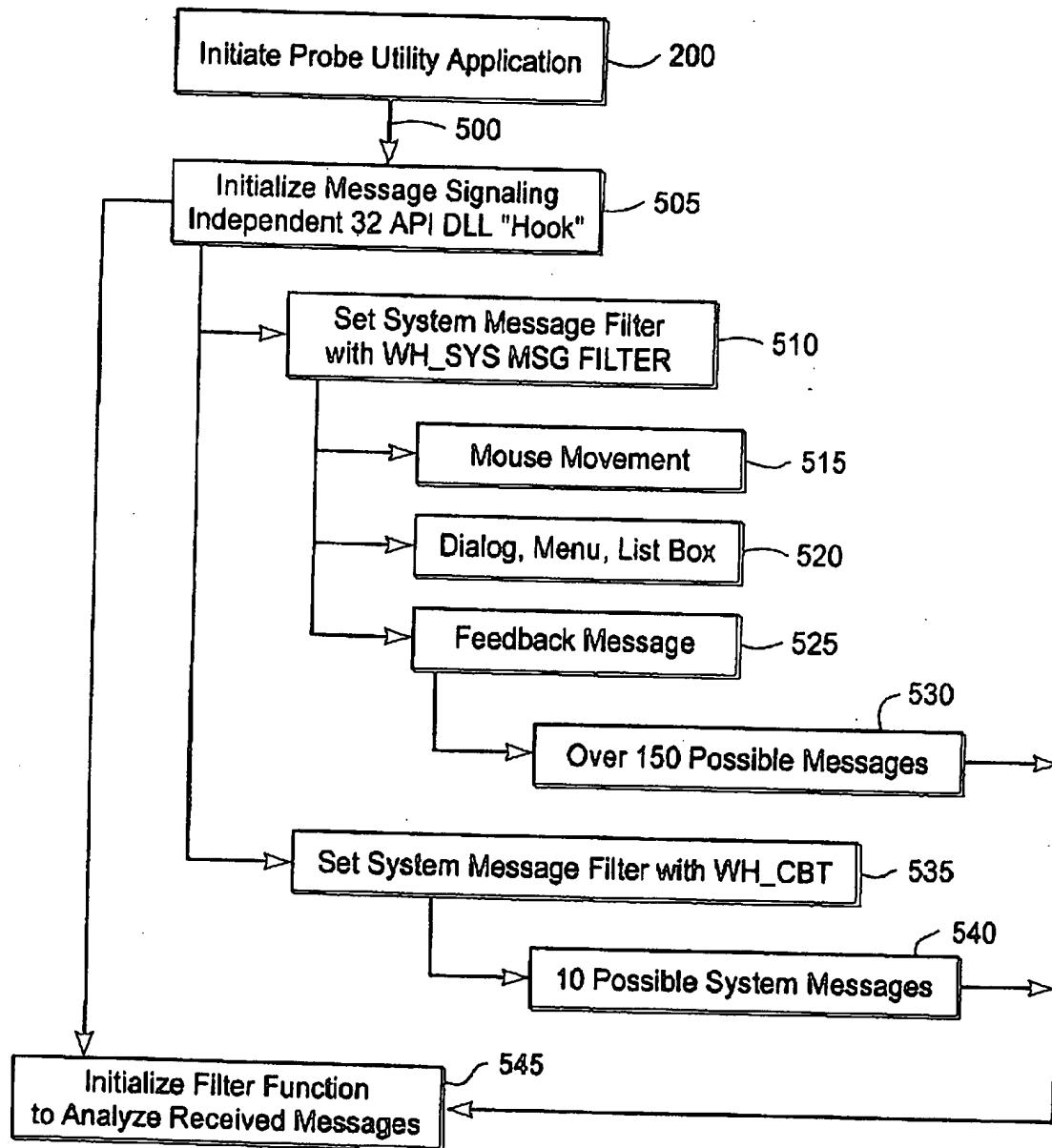


FIG. 5

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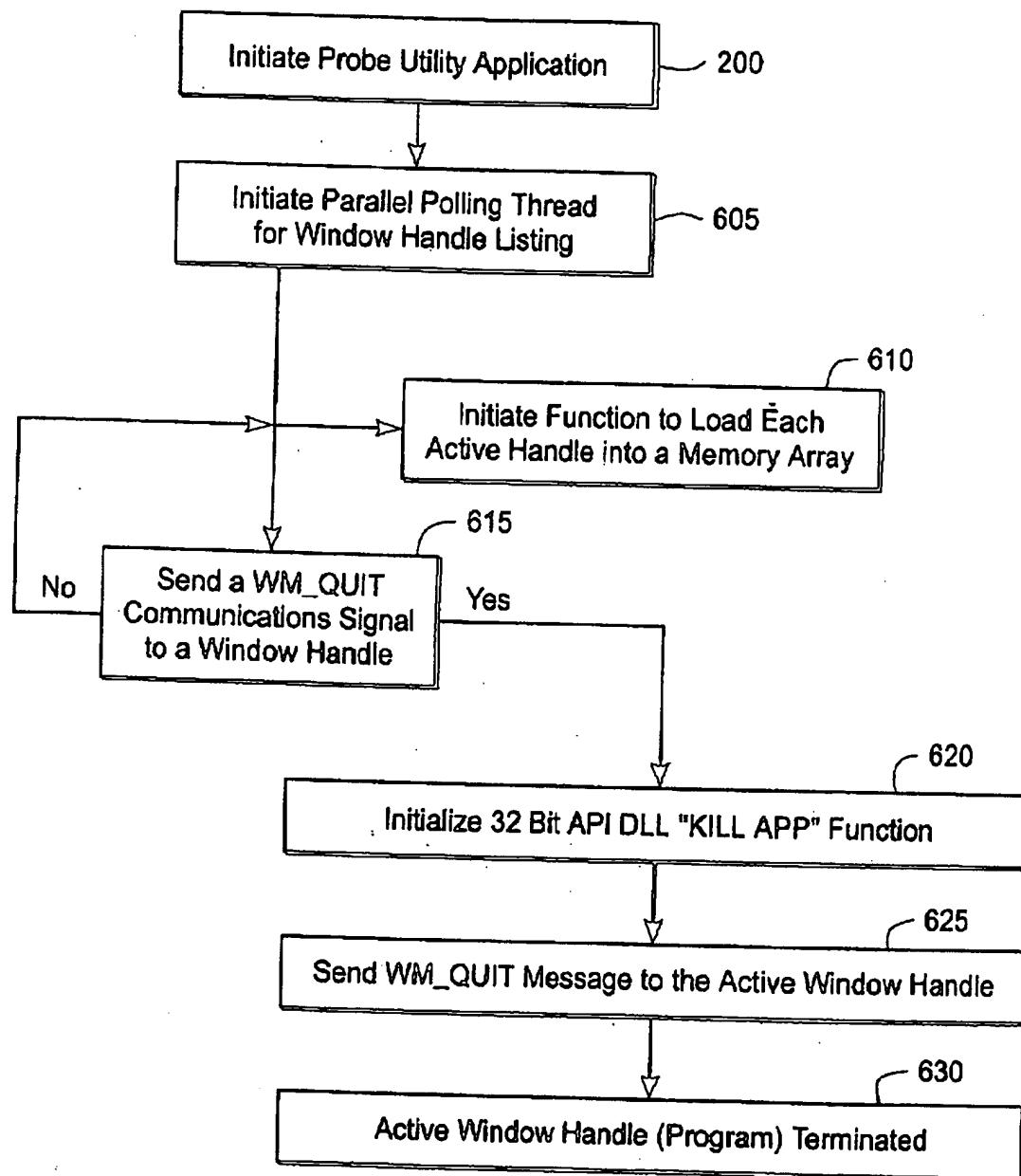


FIG. 6

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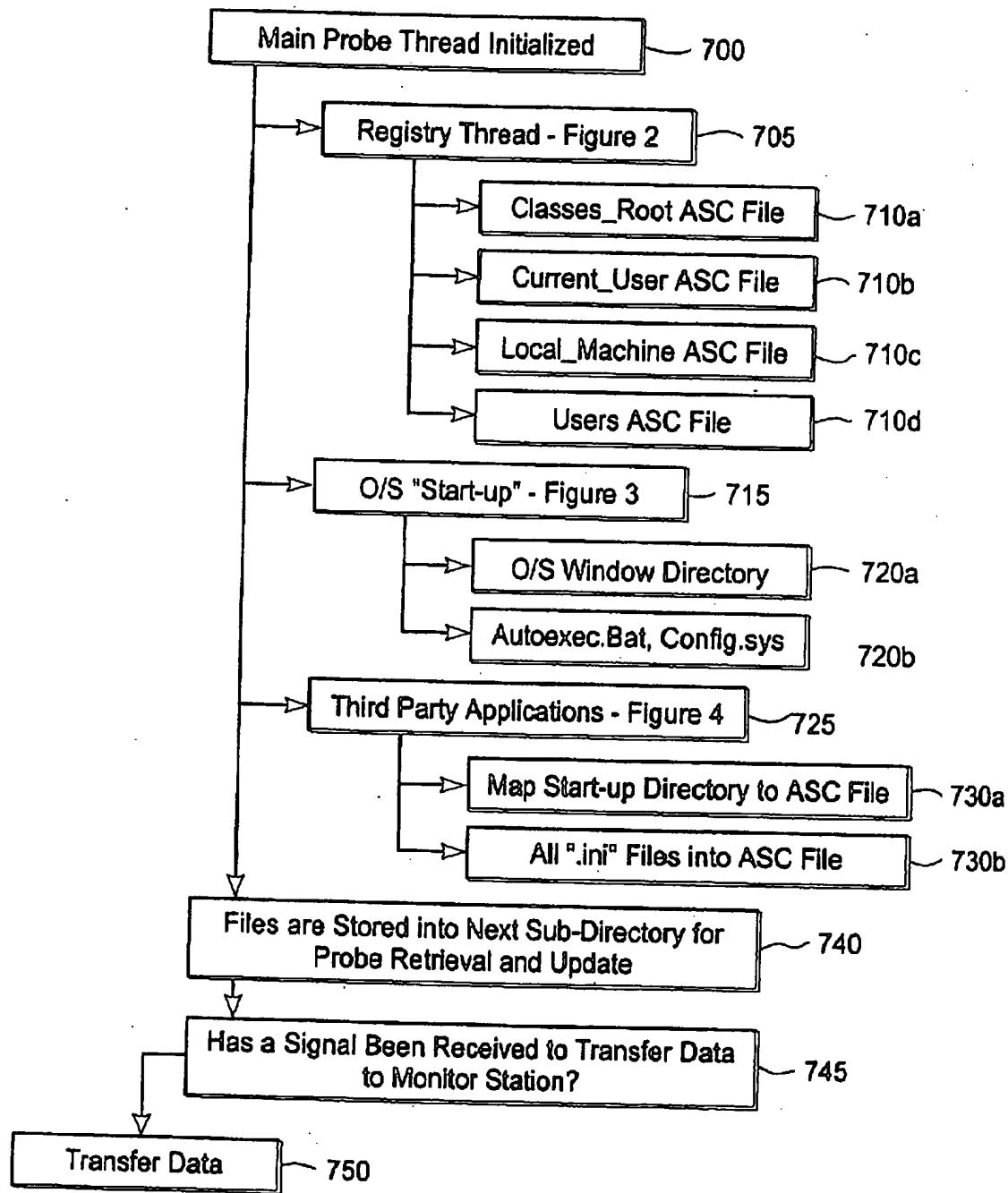


FIG. 7

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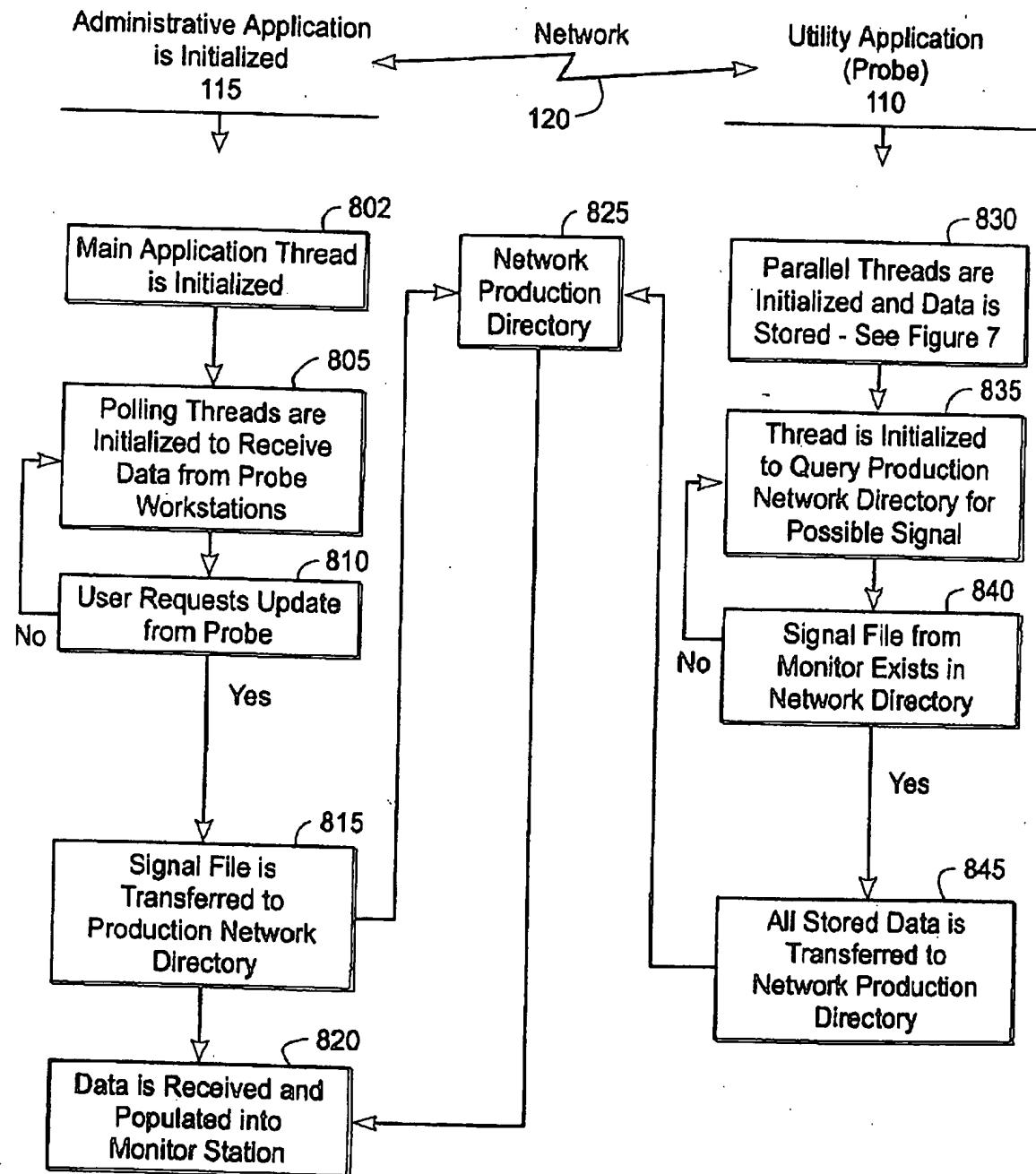


FIG. 8

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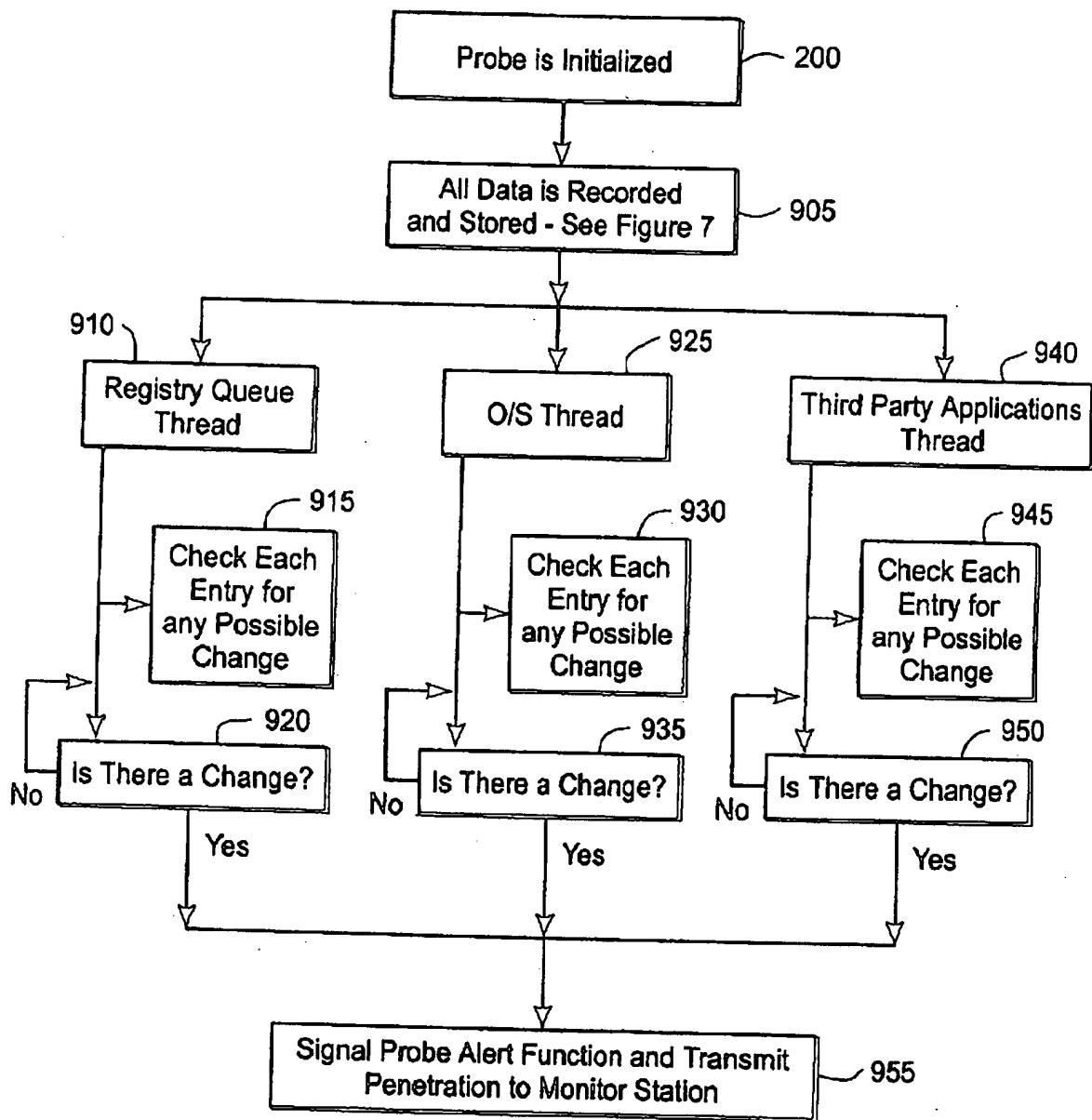


FIG. 9

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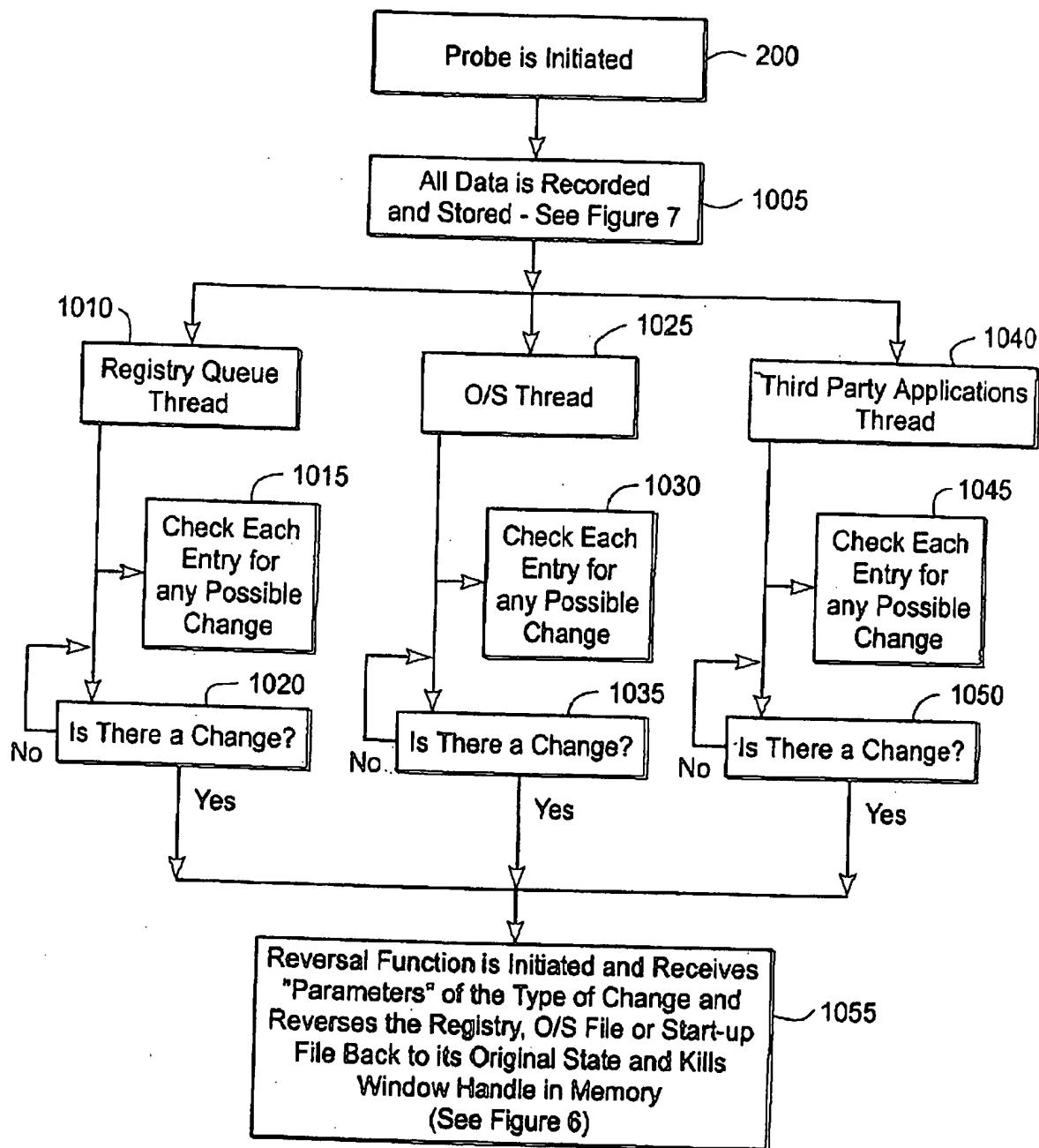


FIG. 10

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1100

SOT [CR] [LF]

Date=CCYY\MM\DD [cr] [lf]

Time=HH : MM : SS [cr] [lf]

3Wind=Variable Up To 500 Characters [cr] [lf]

2Wind=Variable Up To 500 Characters [cr] [lf]

1Wind=Variable Up To 500 Characters [cr] [lf]

Mssg=Variable Up To 500 Characters [cr] [lf]

EOT [cr] [lf]

Structured Signal File Block Diagram

FIG. 11

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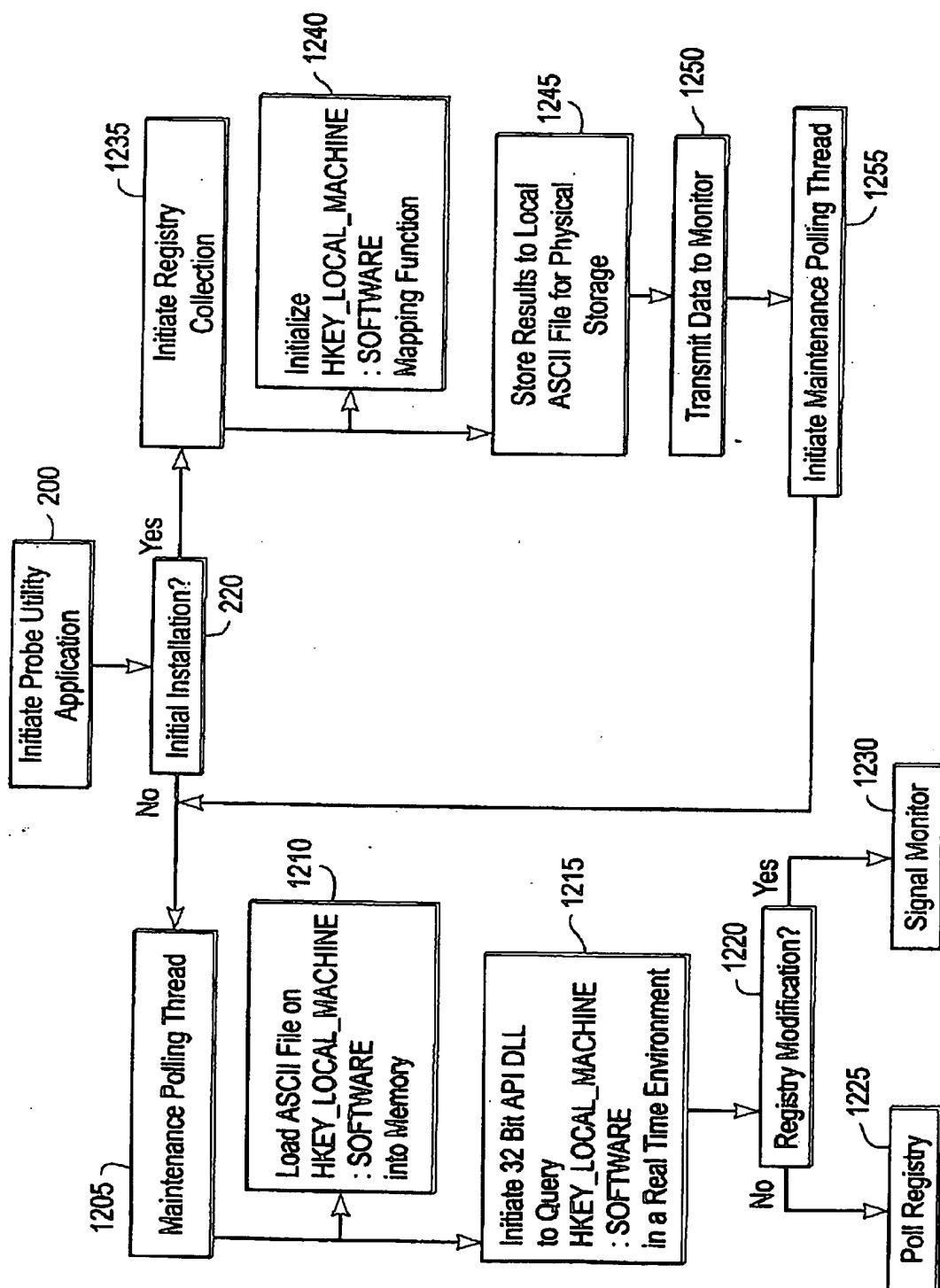


FIG. 12

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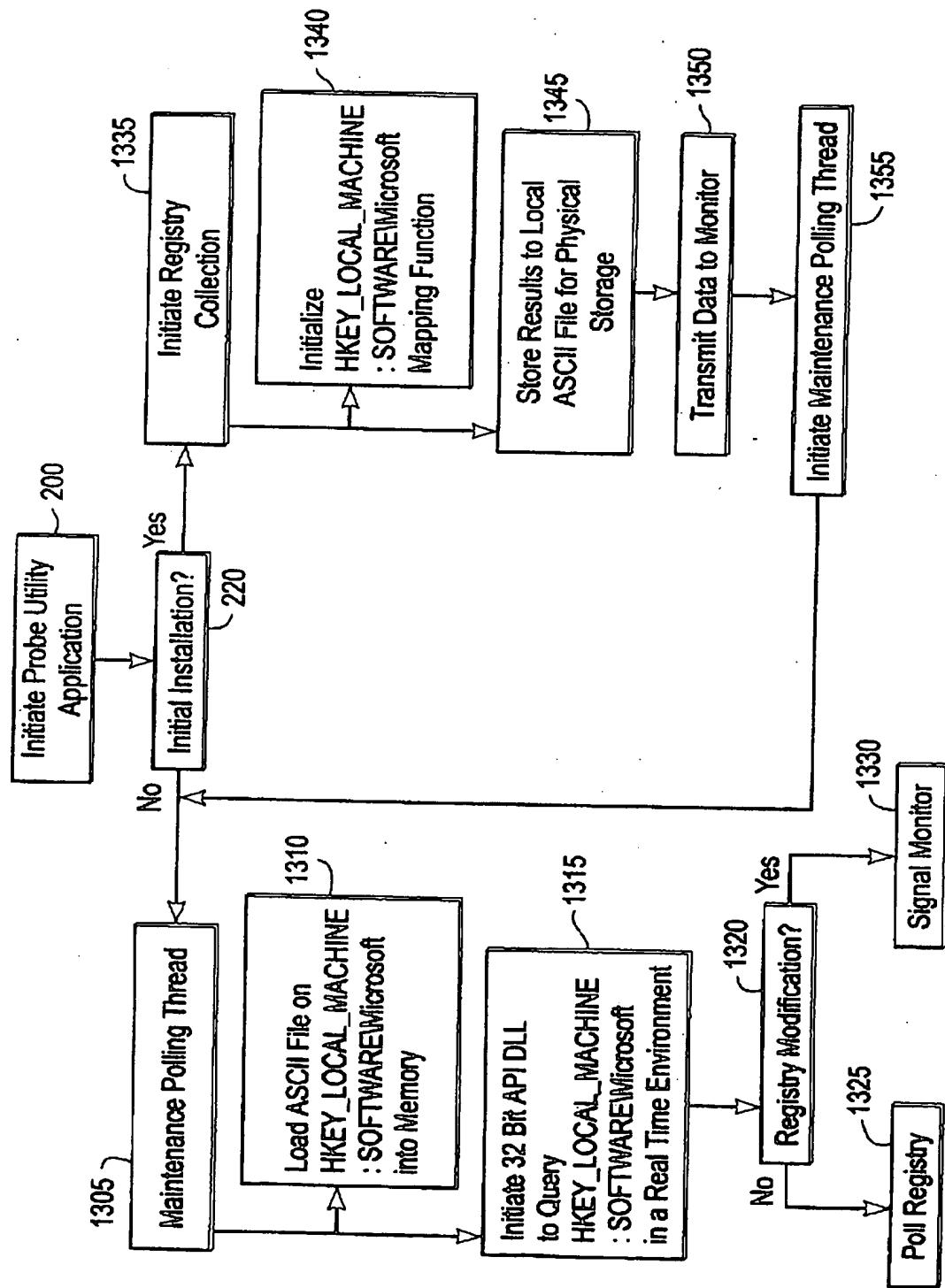


FIG. 13

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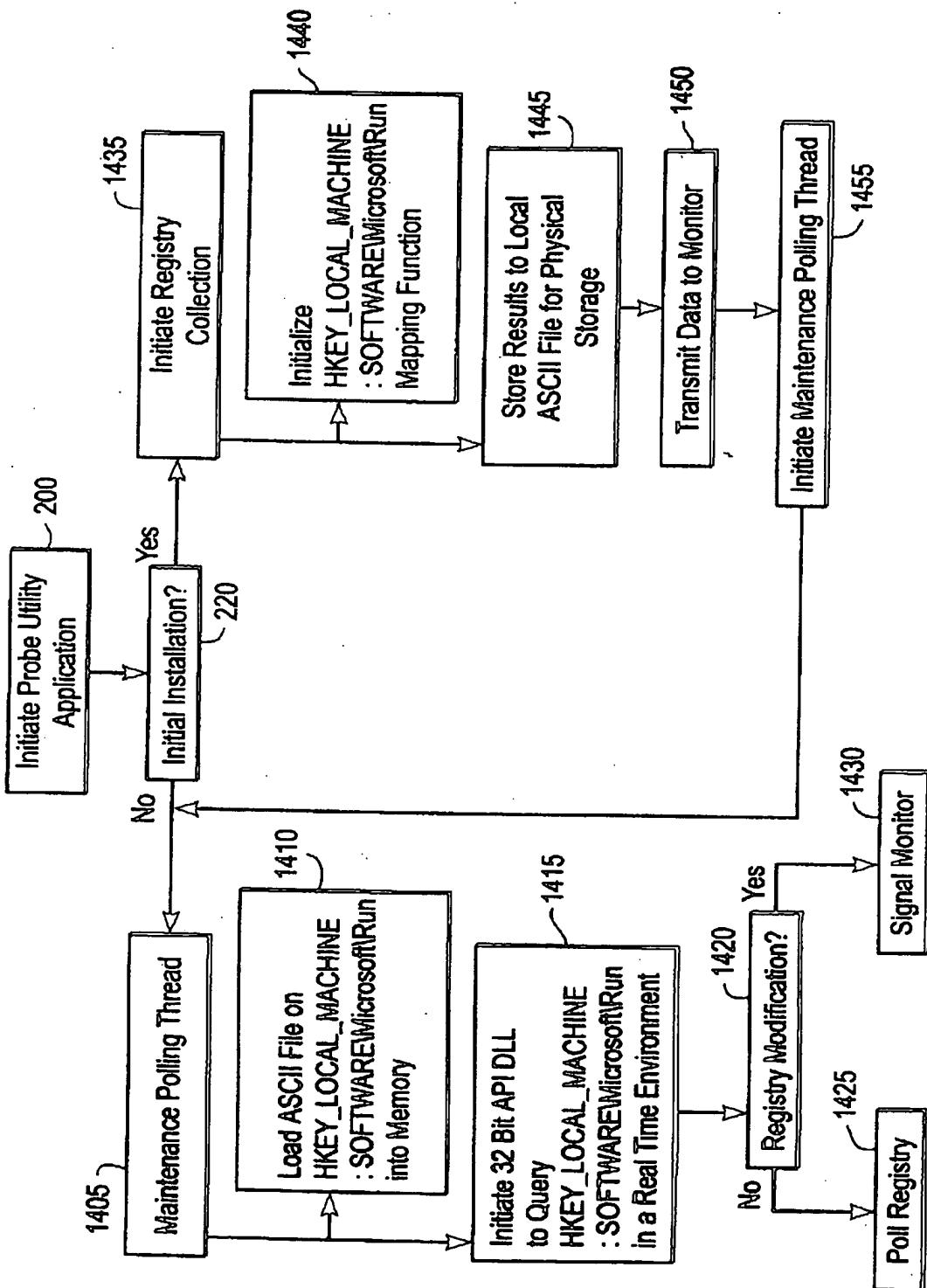


FIG. 14

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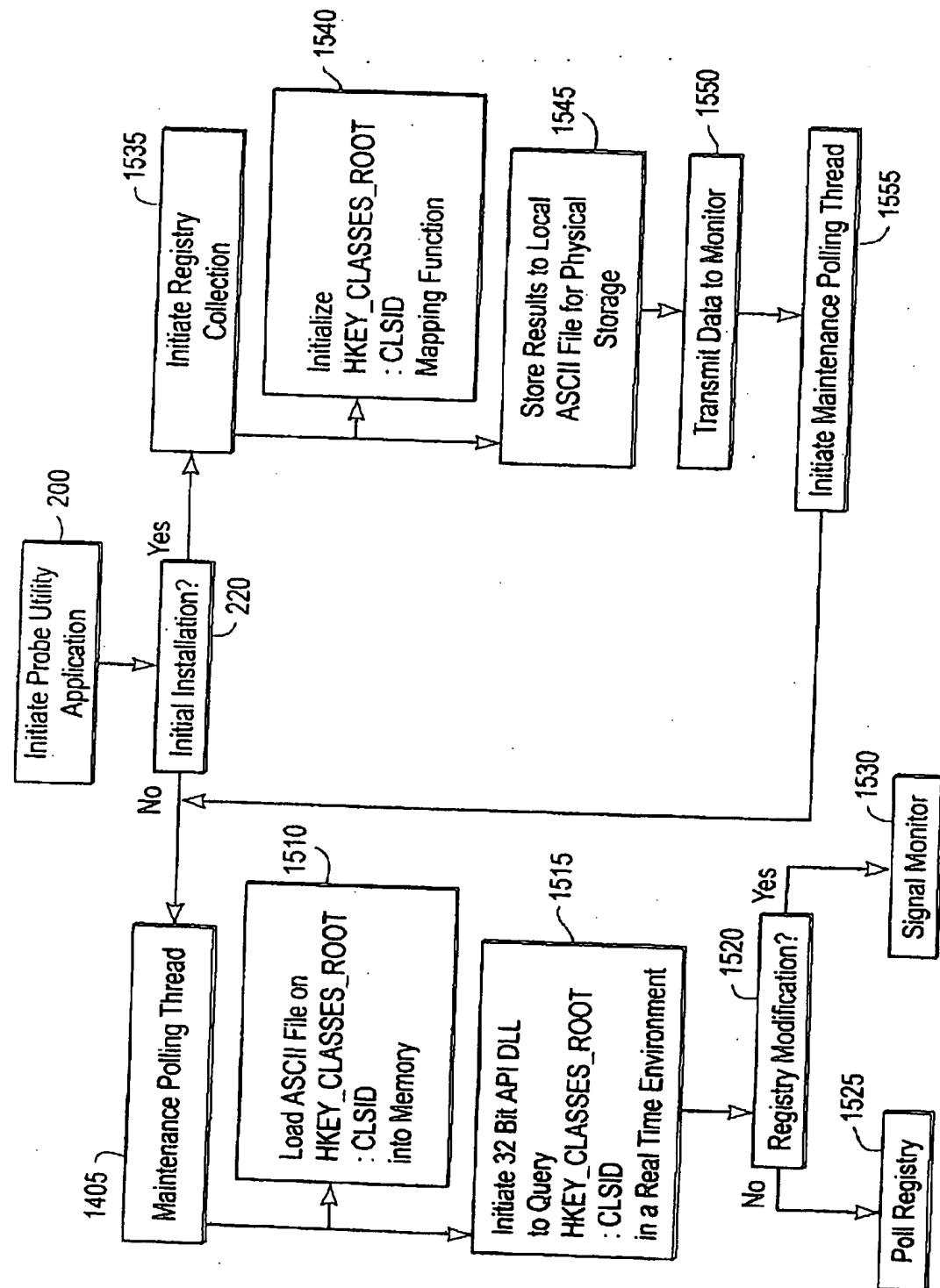


FIG. 15

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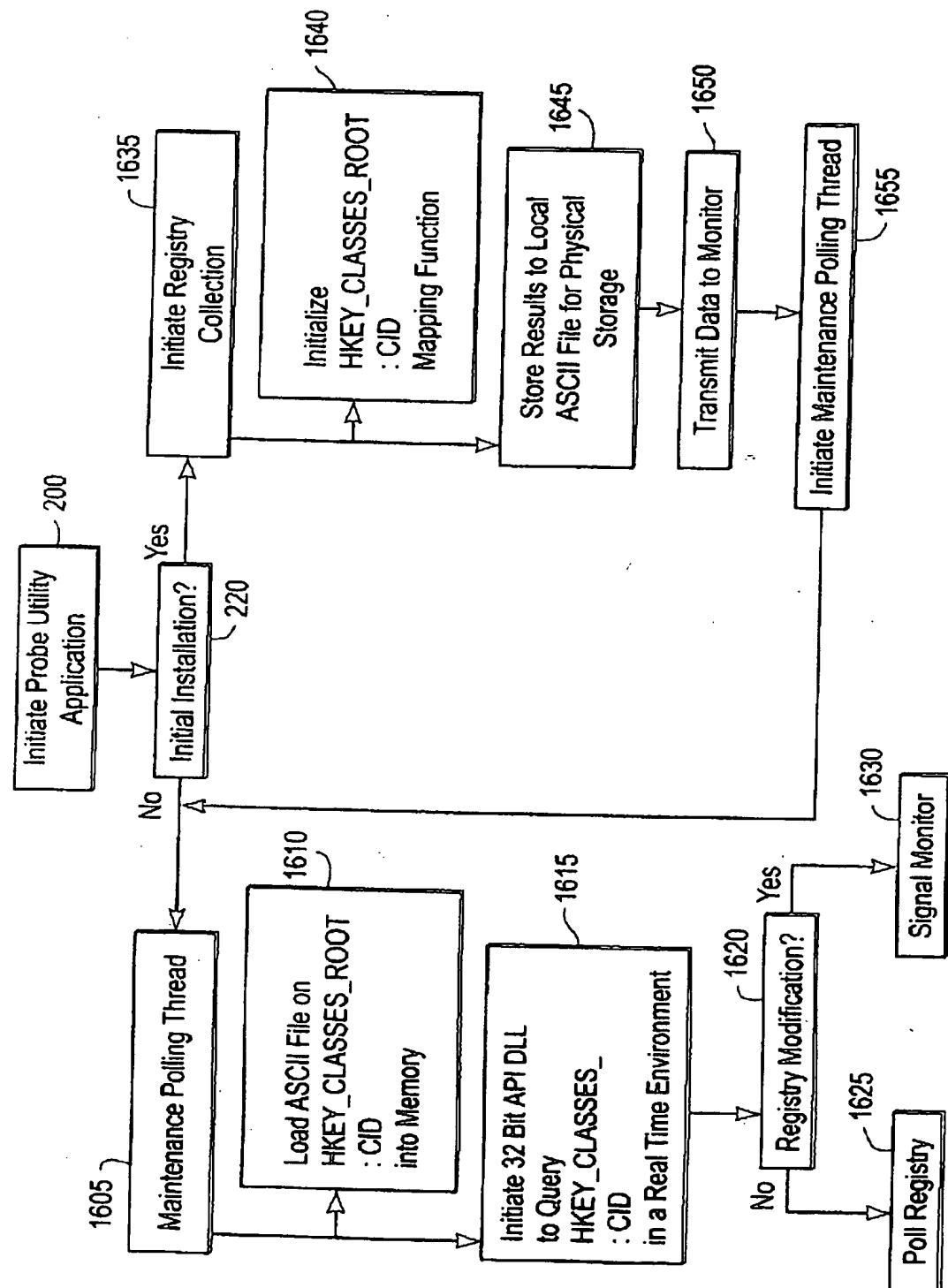


FIG. 16

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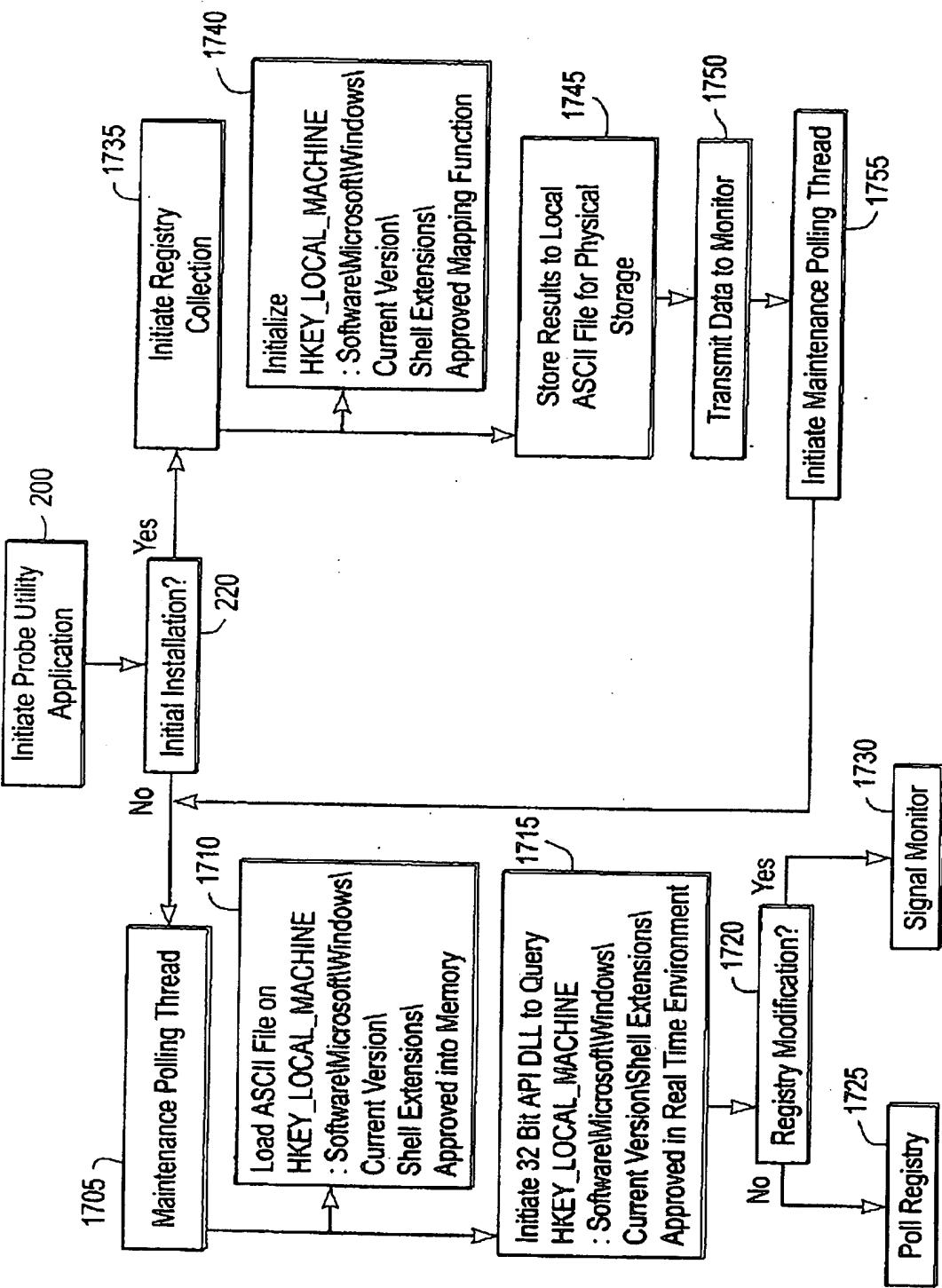


FIG. 17

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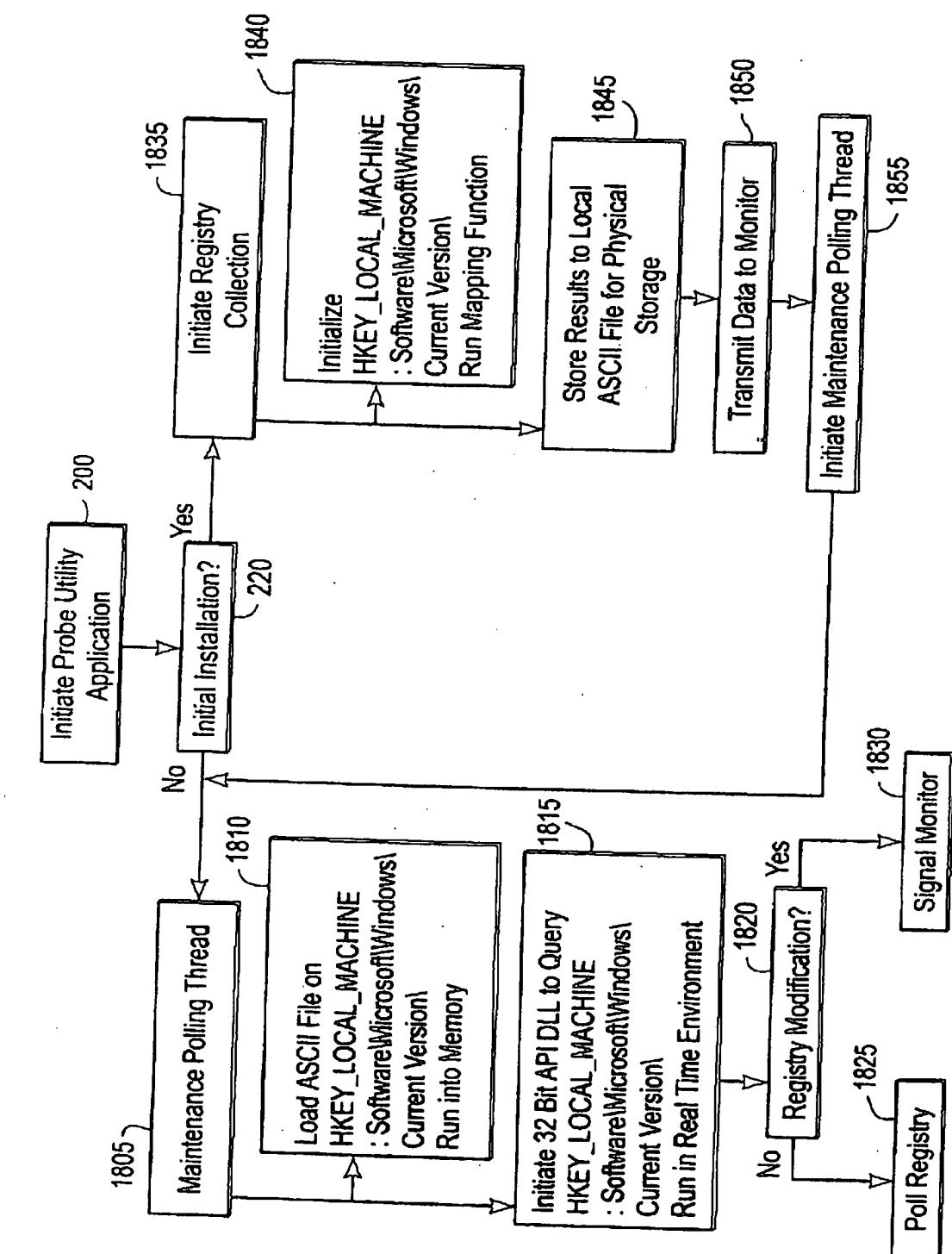


FIG. 18

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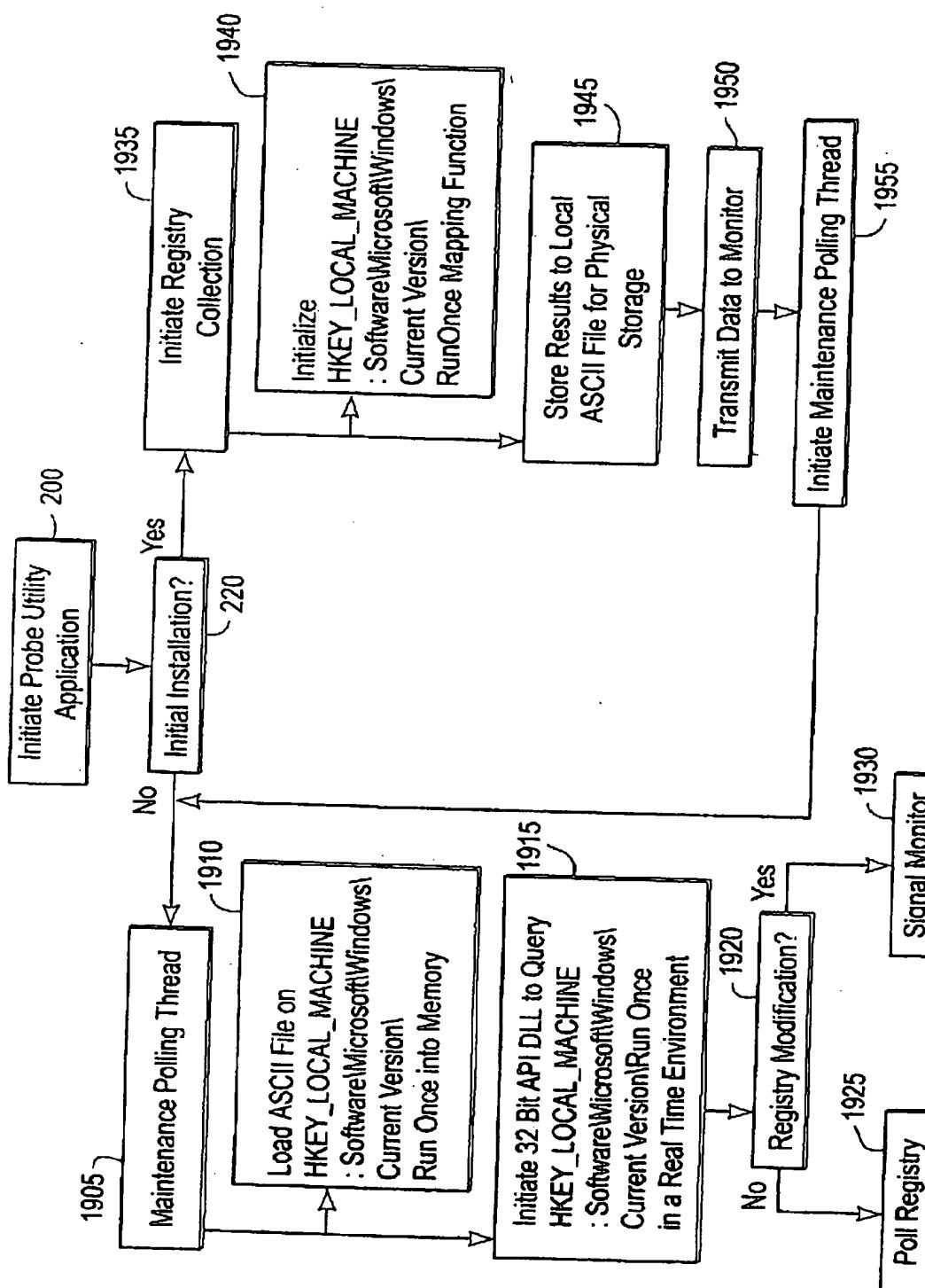


FIG. 19

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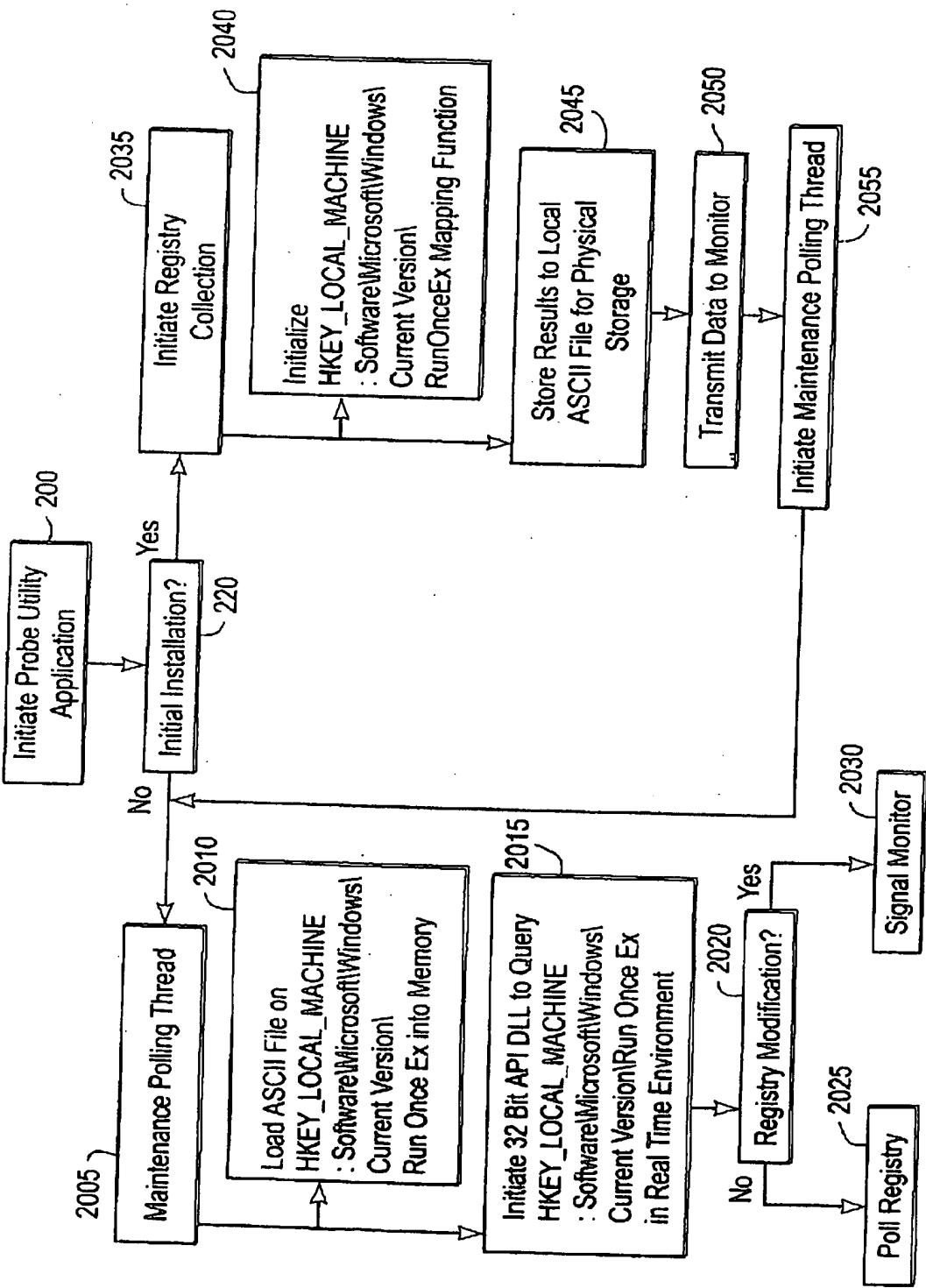


FIG. 20

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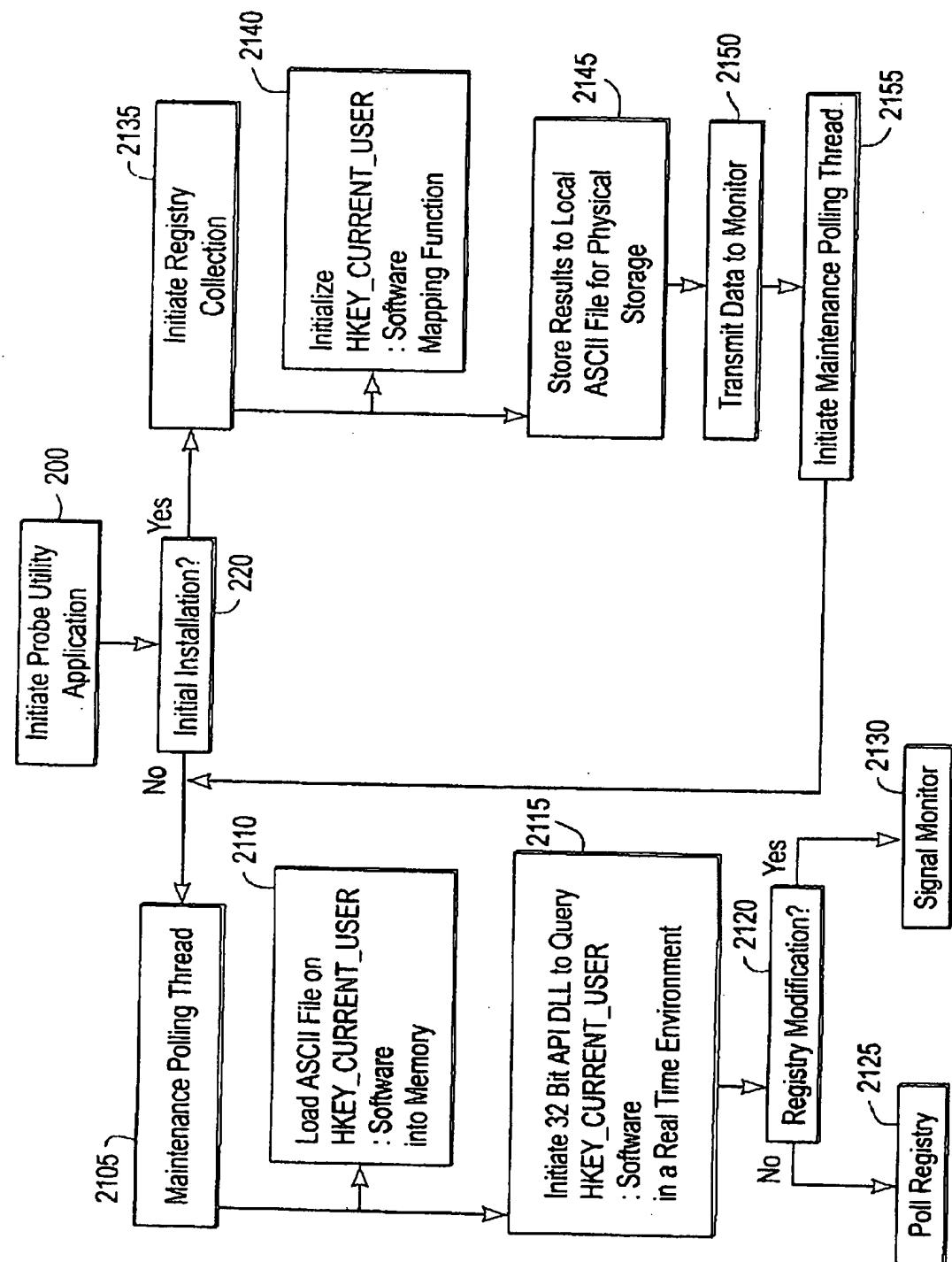


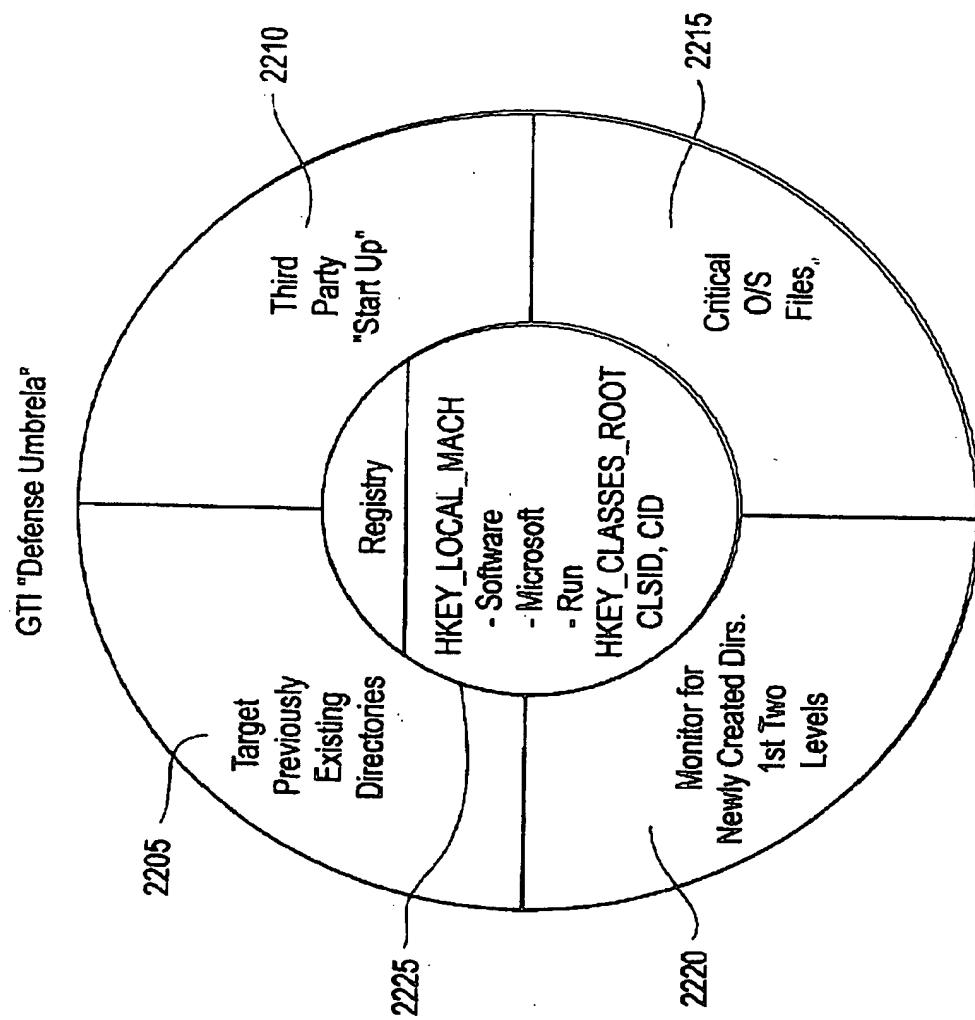
FIG. 21

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+



"Guarded State" of the Complete PC

**FIG. 22**

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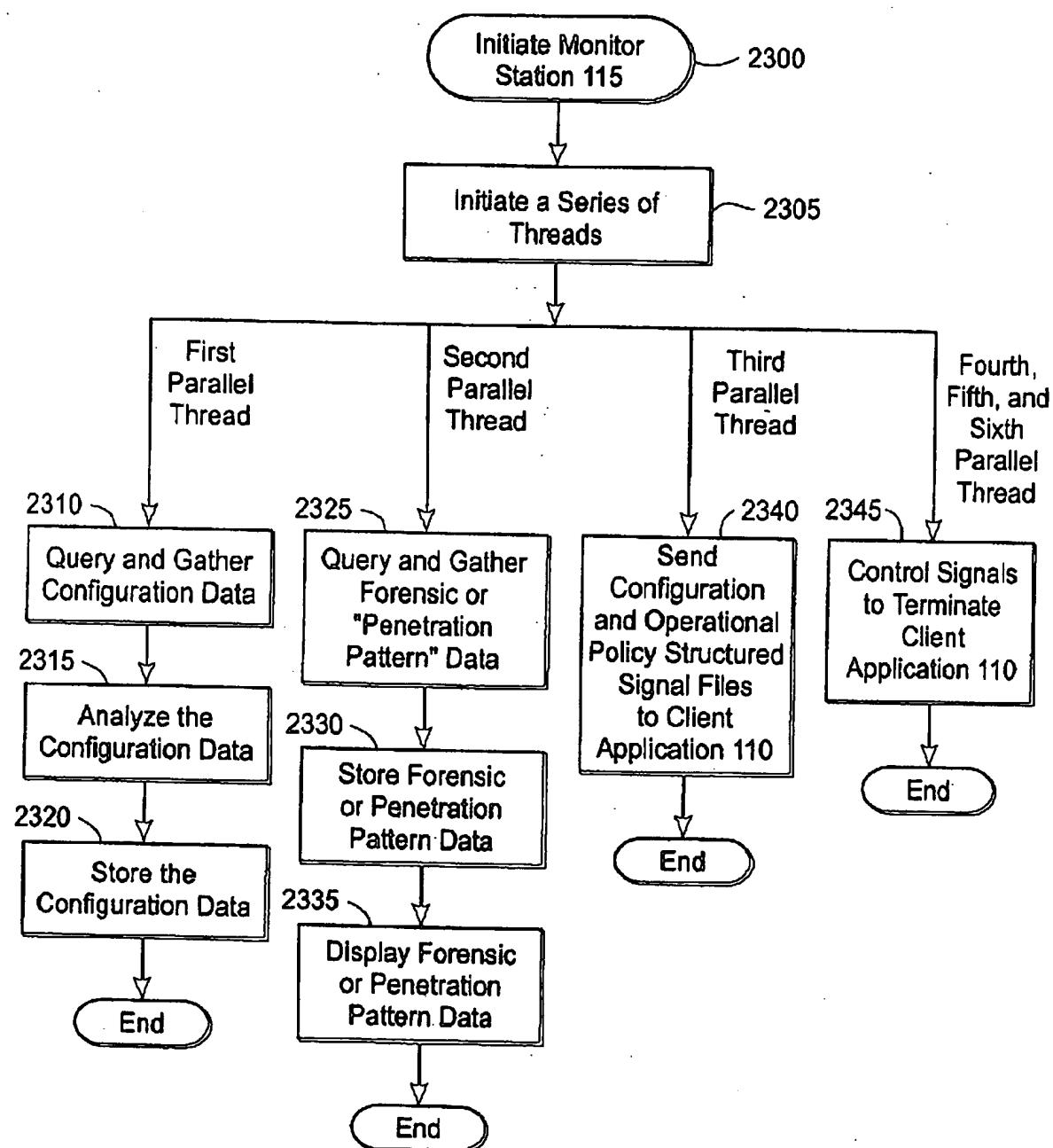


FIG. 23

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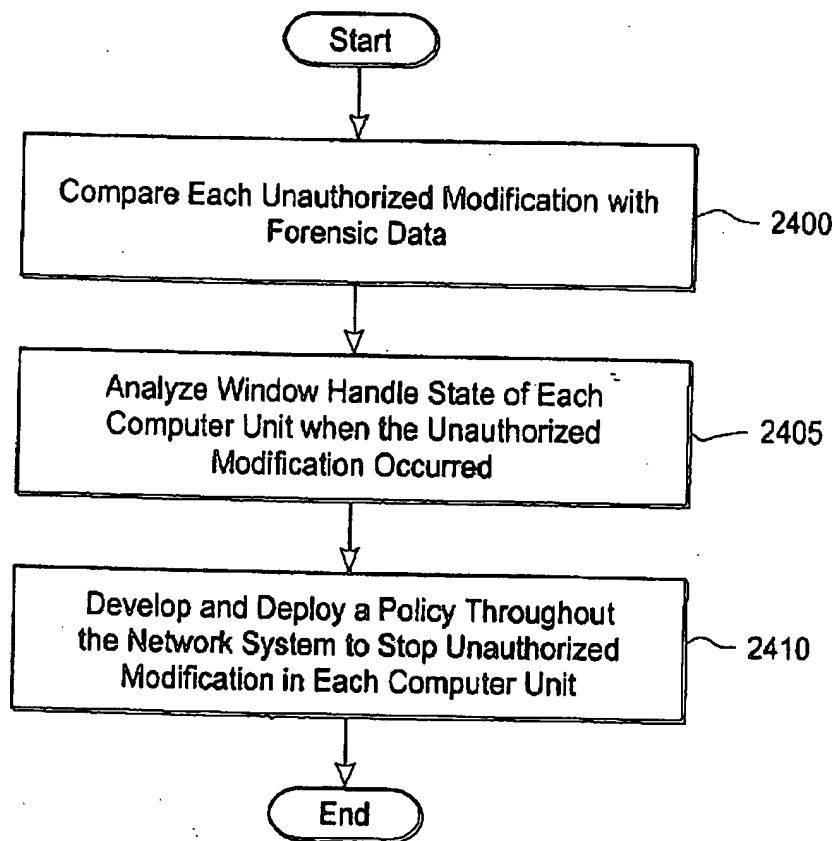


FIG. 24